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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Tim Allen

Atty Dkt No.: ALTRP108/A1108

Application No.: 10/749,857

Examiner: To be assigned

Filed: December 30, 2003

Group: 2182

Title: STREAMING OUTPUT PERIPHERALS
FOR PROGRAMMABLE CHIP SYSTEMS

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on January 12, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____

Joyce L. Ferreira

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

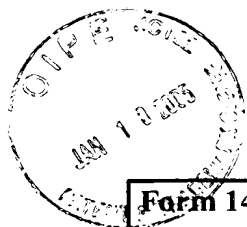
This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. ALTRP108).

Respectfully submitted,

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No.	Application No.:
	ALTRP108/A1108	10/749,857
	Applicant:	
	Tim Allen	
	Filing Date	Group
	December 30, 2003	Not Assigned

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	J							
	K							
	L							
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	O	DsPIC® Digital Signal Controllers, 16-bit Microcontrollers, website www.microchip.com , December 1, 2004, 1-3 pages
	P	PICmicro® Microcontrollers, 8-bit Microcontrollers, website www.microchip.com , December 1, 2004, 1-3 pages
	Q	Microchip dsPIC30F3014, dsPIC30F4013 Data Sheet, High Performance Digital Signal Controllers, Advance Information, © 2004 Microchip Technology Inc. 212 pages
	R	Motorola TPU Reference Manual TPURM/AD 199067 pages
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.